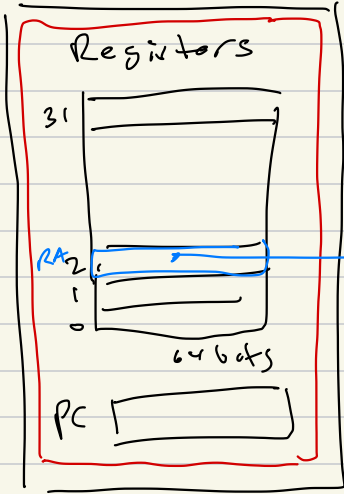
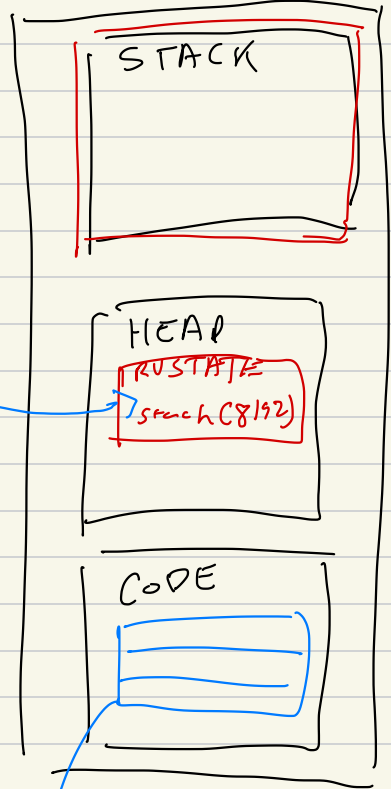


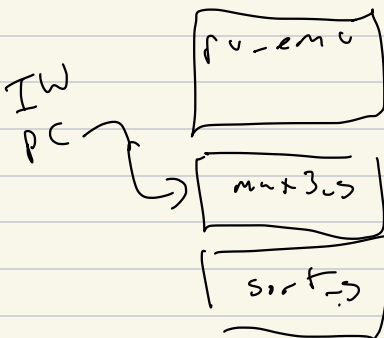
Processor
RISC-V 64bit



Memory (RAM)



Machine instructions
add a0,a0,a1



switch (n) {

case 0:

case 1:

case 2:

default:

}

Switch (n)

case 1;

case 100;

case 10000;

case 10100;

default

}